L Number	Hits	Search Text	DB	Time stamp
1	26	(pipe adj latch) and register	USPAT; EPO; JPO; DERWENT;	2004/06/27 15:35
2	8	latch and (linkage adj register)	USOCR USPAT; EPO; JPO; DERWENT; USOCR	2004/06/27 15:36
3	3846	latch and (input adj register)	USPAT; EPO; JPO; DERWENT; USOCR	2004/06/27 15:36
4	920	(latch and (input adj register)) and (rising adj edge)	USPAT; EPO; JPO; DERWENT; USOCR	2004/06/27 15:37
5	477	((latch and (input adj register)) and (rising adj edge)	USPAT; EPO; JPO; DERWENT; USOCR	2004/06/27 15:37
6	345	<pre>(((latch and (input adj register)) and (rising adj edge)) and (falling adj edge)) and multiplexer</pre>	USPAT; EPO; JPO; DERWENT; USOCR	2004/06/27 15:37
7	309	((((latch and (input adj register)) and (rising adj edge)) and (falling adj edge)) and multiplexer) and seri\$4	USPAT; EPO; JPO; DERWENT; USOCR	2004/06/27 15:38
8	280	<pre>(((((latch and (input adj register)) and (rising adj edge)) and (falling adj edge)) and multiplexer) and seri\$4) and even</pre>	USPAT; EPO; JPO; DERWENT; USOCR	2004/06/27 15:38
9	151	<pre>((((((latch and (input adj register)) and (rising adj edge)) and (falling adj edge)) and multiplexer) and seri\$4) and even) and odd</pre>	USPAT; EPO; JPO; DERWENT; USOCR	2004/06/27 15:38
10	144	<pre>(((((((latch and (input adj register)) and (rising adj edge)) and (falling adj edge)) and multiplexer) and seri\$4) and even) and odd) and sequential\$3</pre>	USPAT; EPO; JPO; DERWENT; USOCR	2004/06/27 15:38
11	38	<pre>(((((((latch and (input adj register)) and (rising adj edge)) and (falling adj edge)) and multiplexer) and seri\$4) and even) and odd) and sequential\$3) and pipe</pre>	USPAT; EPO; JPO; DERWENT; USOCR	2004/06/27 15:40
12	0	(serial adj pipe adj latch\$2) and register	USPAT; EPO; JPO; DERWENT; USOCR	2004/06/27 15:41
13	1	<pre>(((((((((latch and (input adj register)) and (rising adj edge)) and (falling adj edge)) and multiplexer) and seri\$4) and even) and odd) and sequential\$3) and linkage</pre>	USPAT; EPO; JPO; DERWENT; USOCR	2004/06/27 15:41